



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

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In re Application

Inventor(s): Suryanarayana Duggirala, Rohit Kapur and Thomas W. Williams

Application No.: TBD

Filed: 03/24/99

Title: SYSTEM AND METHOD FOR HIGH-LEVEL TEST PLANNING FOR LAYOUT

Assistant Commissioner for Patents  
Washington, D.C. 20231

NOTIFICATION OF FILING OF CONTINUING APPLICATION

**RECEIVED**

☒ Notification is hereby being made of the filing of a continuation application for this case **APR 28 1999**

☒ concurrently herewith.  
☐ on .....

**TECHNOLOGY CENTER 2800**

Please direct all correspondence concerning the above-identified application to the following address:

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Respectfully submitted,

Date: March 31, 1999

By: *Wilfred H. Lam*  
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**JUN 29 1999**

**Group 2700**